



21861 U.S. PTO

PTO/SB/05 (03-01)

Approved for use through 10/31/2002. OMB 0651-0032
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UTILITY
PATENT APPLICATION
TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.

A1168

First Inventor

Zunghang Yu, et. al.

Title

Apparatus and Method for RTL Based Full Chip.
Modeling of a Programmable Logic Device

Express Mail Label No.

EL 976984794

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. ☒ Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original and a duplicate for fee processing)
2. ☐ Applicant claims small entity status.
See 37 CFR 1.27.
3. ☒ Specification [Total Pages 19]
(preferred arrangement set forth below)
- Descriptive title of the invention
 - Cross Reference to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to sequence listing, a table, or a computer program listing appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
4. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets 10]
5. Oath or Declaration [Total Pages 3]
- a. ☒ Newly executed (original or copy)
- b. ☐ Copy from a prior application (37 CFR 1.63 (d))
(for continuation/divisional with Box 18 completed)
- i. ☐ **DELETION OF INVENTOR(S)**
Signed statement attached deleting inventor(s)
named in the prior application, see 37 CFR
1.63(d)(2) and 1.33(b).
6. ☐ Application Data Sheet. See 37 CFR 1.76

ADDRESS TO:

Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

7. ☐ CD-ROM or CD-R in duplicate, large table or
Computer Program (Appendix)
8. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
- a. ☐ Computer Readable Form (CRF)
- b. Specification Sequence Listing on:
- i. ☐ CD-ROM or CD-R (2 copies); or
 - ii. ☐ paper
- c. ☐ Statements verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

9. ☒ Assignment Papers (cover sheet & document(s))
10. ☐ 37 CFR 3.73(b) Statement [Power of Attorney]
(when there is an assignee)
11. ☐ English Translation Document (if applicable)
12. ☐ Information Disclosure Statement (IDS)/PTO-1449 [Copies of IDS Citations]
13. ☐ Preliminary Amendment
14. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
15. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
16. ☒ Nonpublication Request under 35 U.S.C. 122
(b)(2)(B)(i). Applicant must attach form PTO/SB/35
or its equivalent.
17. ☐ Other:

18. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:

☐ Continuation☐ Divisional☐ Continuation-in-part (CIP)

of prior application No.: _____

Prior application information:

Examiner: _____

Group Art Unit: _____

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

19. CORRESPONDENCE ADDRESS



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4-8-04

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

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040804

Express Mail Label
No. EL 976984794

ATTORNEY DOCKET NO. A1168

Date: April 8, 2004

Assistant Commissioner for Patents
Box PATENT APPLICATION
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following utility patent application is enclosed for filing:

Applicant(s): Zunghang Yu Executed on: March 8, 2004
Title of Invention: **Apparatus and Method for RTL Based Full Chip Modeling of a**
Programmable Logic Device

PATENT APPLICATION FEE VALUE

TYPE	No. FILED	LESS	EXTRA	EXTRA RATE (each)	FEE
Total Claims	16	-20	0	\$18.00	\$0.00
Independent Claims	2	-3	0	\$84.00	\$0.00
				Minimum Fee	\$750.00
				Multiple Dependency Fee, If applicable (\$250.00)	\$0.00
				TOTAL	\$750.00

Please charge the required fee to Altera Corporation Deposit Account 50-2300. A copy of this sheet is enclosed.

Respectfully submitted,



Archana Ojha
Patent Attorney,
Altera Corporation

53,408

Reg. No.

Enclosure

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Zunghang Yu, et. al.	Application No.: Not Yet Assigned
Filed: Herewith	Confirmation No.: Not Yet Assigned
For: Apparatus and Method for RTL Based Full Chip Modeling of a Programmable Logic Device	Applicant Reference No.: A1168

San Jose, CA 95134

Date: April 8, 20004

Assistant Commissioner for Patents
Box PATENT APPLICATION
P.O. Box 1450
Alexandria, VA 22313-1450

NON-PUBLICATION REQUEST UNDER 37 C.F.R. §1.213(a)

Sir:

Pursuant to 37 C.F.R. §1.213(a), applicants hereby request that the above-identified patent application, which is being filed concurrently herewith, not be published under 35 U.S.C. §122(b). It is hereby certified that the invention disclosed in the application has not been and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

Respectfully submitted,



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